



Atty. Dkt. No. 039153-0433 (C167596-CIP)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Sander et al.

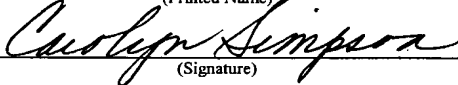
Title: MINIMIZING TRANSISTOR
SIZE IN INTEGRATED
CIRCUITS

Appl. No.: 10/042,732

Filing Date: 04/25/2001

Examiner: Nguyen, T.

Art Unit: 2813

CERTIFICATE OF EXPRESS MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37.C.F.R. § 1.10 on the date indicated below and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
EV 593172636 US	1/25/06
(Express Mail Label Number)	(Date of Deposit)
Carolyn Simpson	
(Printed Name)	
	
(Signature)	

AMENDMENT UNDER 37 CFR 1.312

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants acknowledge receipt of a Notice of Allowance in the above-captioned application. Prior to payment of the issue fee, please amend the application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this document.

Remarks/Arguments begin on page 6 of this document.

Please amend the application as follows: